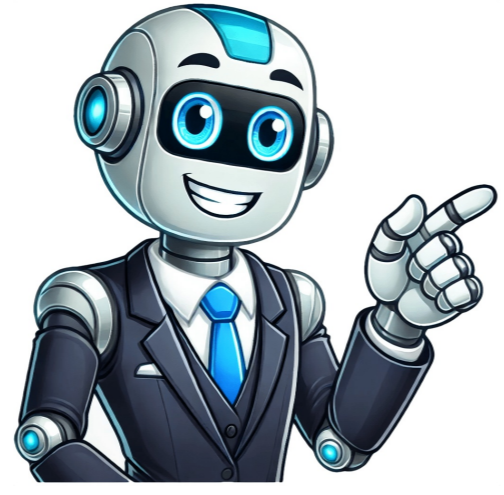


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YJZ_0305_2006-03-19 03:11:40 FC4FortranFortran ... jiangrains 2009-07-21 05:58:59 creatfdwriteread error !: Bad file descriptor #include#include#include#includeint main(){ int fd; if((fd=creat("file1.c",S_IRWXU))===-1){ perror("creat error !"); exit(0); }else { //int fd=open("file1.c",O_RDWR); if(write(fd,"ni hao !",sizeof("ni hao !")-1)===-1){ perror("write error !"); exit(0); }else{ if(write(fd,"jiangdunchuan !",sizeof("jiangdunchuan !")==-1){ perror("error !"); exit(0); } printf("successful !"); } char buff[100]; //int fd=open("file1.c",O_RDWR); if(read(fd,buff,100)===-1){ perror("read error !"); exit(0); }else{ printf("%s",buff); printf("ok"); } } return 0;} read error !: Bad file descriptorfd2readread(fd2,buff,100)===-1 ... liangliang_2006_2007-08-09 10:38:16 teechart7builder6Access violation at address in module 'Project1.exe' Read of address 000000.TChart50cpu985121G512 ... 2017-01-23 10:17:49 Unix-P73written() if ( (nwritten = write(fd, ptr, nleft)) id:11, dlc:1, data:20 idsend fail! CAN-----void CanComm::init(void)(int ret;struct sockaddr_can addr;struct ifreq ifr; s = socket(PF_CAN, SOCK_RAW, CAN_RAW);if(s == -1){ _DEBUG printf("invalid socket");exit(1);}strcpy(ifr.ifr_name, "can0");ret = ioctl(s, SIOCGIFINDEX, &ifr); _DEBUG printf("ioctl:%d", ret); _DEBUG printf("can0 can_ifindex = %x",ifr.ifr_ifindex);addr_can_family = AF_CAN;addr_can_ifindex = ifr.ifr_ifindex; int loopback = 0; /* 0 = disabled, 1 = enabled (default) */ret = setsockopt(s, SOL_CAN_RAW, CAN_RAW_LOOPBACK, &loopback, sizeof(loopback)); // Setting lookback options: _DEBUG printf("set can0 sock loopback unable:%d", ret); int recv_own_msgs = 0; /* 0 = disabled (default), 1 = enabled */ret = setsockopt(s, SOL_CAN_RAW, CAN_RAW_RECV_OWN_MSGS, &recv_own_msgs, sizeof(recv_own_msgs));printf("set can0 do not receiver own message:%d", ret); bind(s, (struct sockaddr *)&addr, sizeof(addr)); _DEBUG printf("bind can0 sock:%d", ret); } void CanComm::send(void){struct can_frame frame; frame.can_id = canid;frame.can_dlc = 1;frame.data[0] = 20; _DEBUG printf("sending-->id:%d, dlc:%d, data:%d", frame.can_id,frame.can_dlc,frame.data[0]);int nbytes = write(s, &frame, sizeof(frame));if(nbytes != sizeof(frame)) _DEBUG printf("send fail");} ... K-dash 2014-10-11 05:14:20 IDE.VS2010-Windows: Console.WriteLine("aaaaa");_Debug.WriteLine.Trace...IDE??? ... xuecheng 2019-04-27 05:13:18 XC7Z010iclg400sp512M QSPI FLASHJTAGBOOT.binFLASH cmd /C program flash -f E:\ZYNO_PRJZ7_TEST\UART_x8\FSLBOOT3.bin -offset 0 -flash type \qspi -single -fshl E:\ZYNO_PRJZ7_TEST\UART_x8\UART_x8.sd\k\FSL\Debug\FSL_elf -cable type \xilinx -tc url TCP:127.0.0.1:3121 ***** Xilinx Program Flash***** Program Flash v2018.1 (64-bit) **** SW Build 2188600 on Wed Apr 4 18:40:38 MDT 2018 ** Copyright 1986-2018 Xilinx, Inc. All Rights Reserved. Connected to hw server @ TCP:127.0.0.1:3121Available targets and devices:Target 0 : jsn-DLC9LP-0000000000000000 Device 0: jsn-DLC9LP-0000000000000000-4ba0477-0 Retrieving Flash info... Initialization done, programming the memory===== mrd->addr=0xF800025C, data=0x00000001 =====BOOT_MODE REG = 0x00000001WARNING: [Xicom 50-100] The current boot mode is QSPI.If flash programming fails, configure device for JTAG boot mode and try again.===== mrd->addr=0xF8007080, data=0x30800100 ===== mrd->addr=0xF800B18, data=0x00000000 =====Downloading FSBL...Running FSBL...Finished running FSBL.===== mrd->addr=0xF800110, data=0x000FA220 =====READ: ARM_PLL_CFG (0xF800110) = 0x000FA220===== mrd->addr=0xF800100, data=0x00028008 =====READ: ARM_PLL_CTRL (0xF800100) = 0x00028008===== mrd->addr=0xF800120, data=0x1F000200 =====READ: ARM_CLK_CTRL (0xF800120) = 0x1F000200===== mrd->addr=0xF800118, data=0x000FA240 =====READ: IO_PLL_CFG (0xF800118) = 0x000FA240===== mrd->addr=0xF800108, data=0x00030008 =====READ: IO_PLL_CTRL (0xF800108) = 0x00030008Info: Remapping 256KB of on-chip-memory RAM memory to 0xFFFFC000,===== mrd->addr=0xF800008, data=0x00000000 ===== mwr->addr=0xF800008, mask=0x000FFFFF, newData=0x0000DF0D===== mwr->addr=0xF8000910, data=0x000001FF ===== mrd->addr=0xF800004, data=0x00000000 ===== mwr->addr=0xF800004, mask=0x000FFFFF, newData=0x0000767B =====MASKWRITE: addr=0xF800004, mask=0x000FFFFF, newData=0x0000767BProblem in running ubootFlash programming initialization failed.ERROR: Flash Operation Failed ...
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